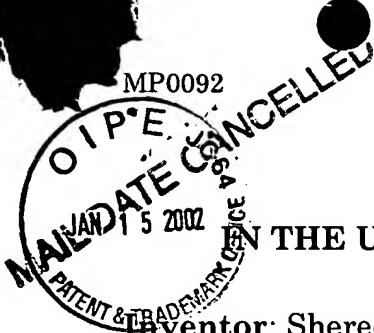


MP0092

62-19-02

2858  
PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sheredy, Joseph

Group Art Unit: Not Yet  
Assigned

Serial No.: 10/042548

Examiner: Not Yet Assigned

Filed: January 9, 2002

Title: Method and Apparatus for Testing a System on a Chip (SOC) Integrated  
Circuit Comprising a Hard Disk Controller and Read Channel

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Commissioner for Patents

Washington, D.C. 20231

Sir:

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The customer number for the above-identified application remains 23624.

Respectfully submitted,

Eric B. Janofsky  
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Date: 2/14/02

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